

**PROCESS FOR FABRICATING A CAPACITOR WITHIN AN INTEGRATED
CIRCUIT, AND CORRESPONDING INTEGRATED CIRCUIT**

Abstract of the Disclosure

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A production of a capacitor includes the simultaneous production, in at least part of an intertrack insulating layer (3) associated with a given metallization level, on the one hand, of the two electrodes (50, 70) and of the dielectric layer (60) of the capacitor and, on the other hand, of a conducting trench (41)

10 which laterally extends the lower electrode of the capacitor, is electrically isolated from the upper electrode and has a transverse dimension smaller than the transverse dimension of the capacitor, and the production, in the interlevel insulating layer (8) covering the intertrack insulating layer, of two conducting pads (80, 81) which come into contact with the upper electrode of
15 the capacitor and with the conducting trench, respectively.